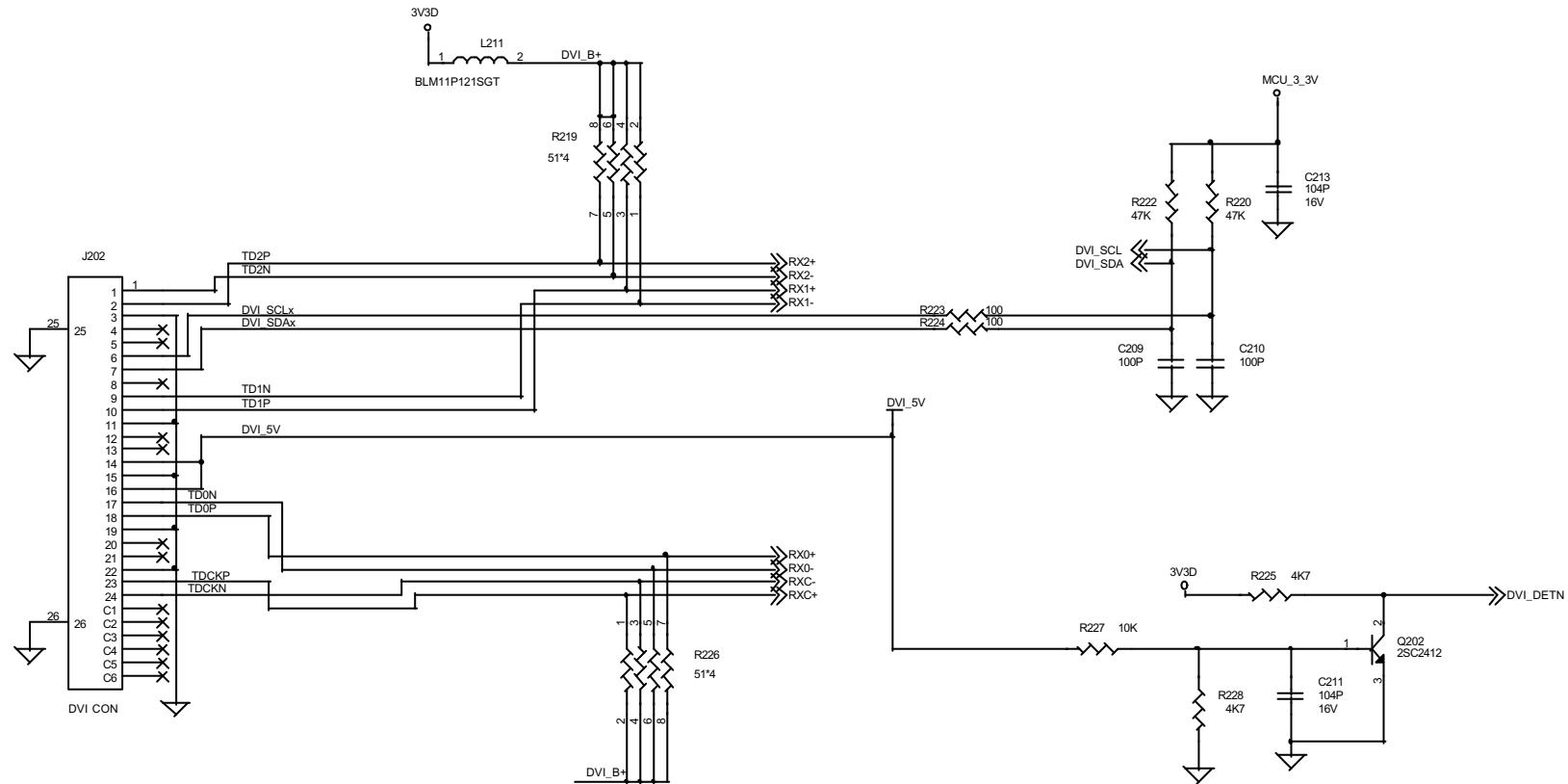
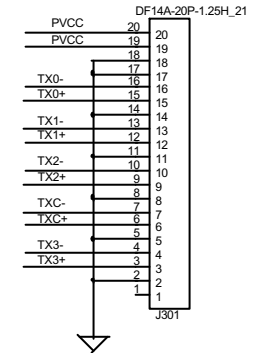
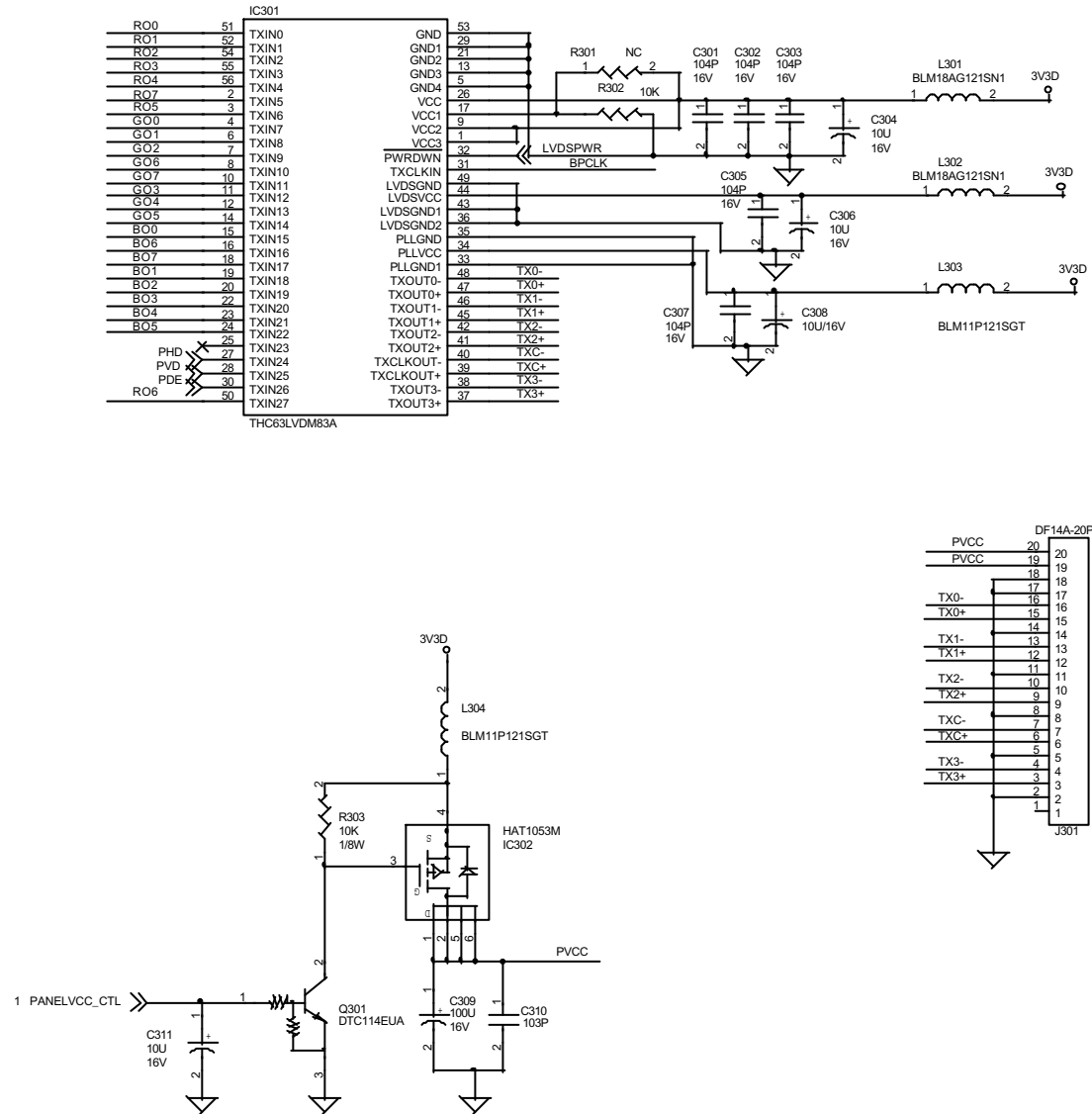
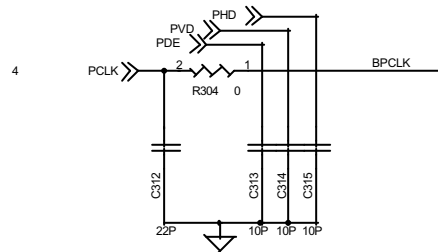
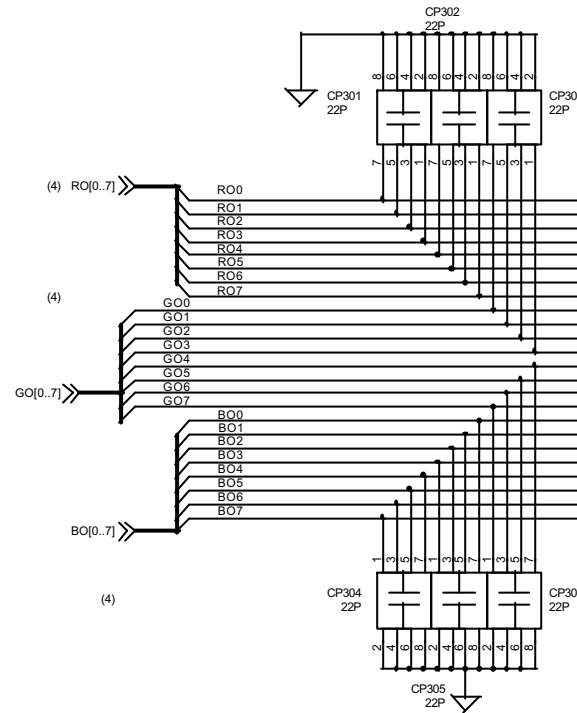


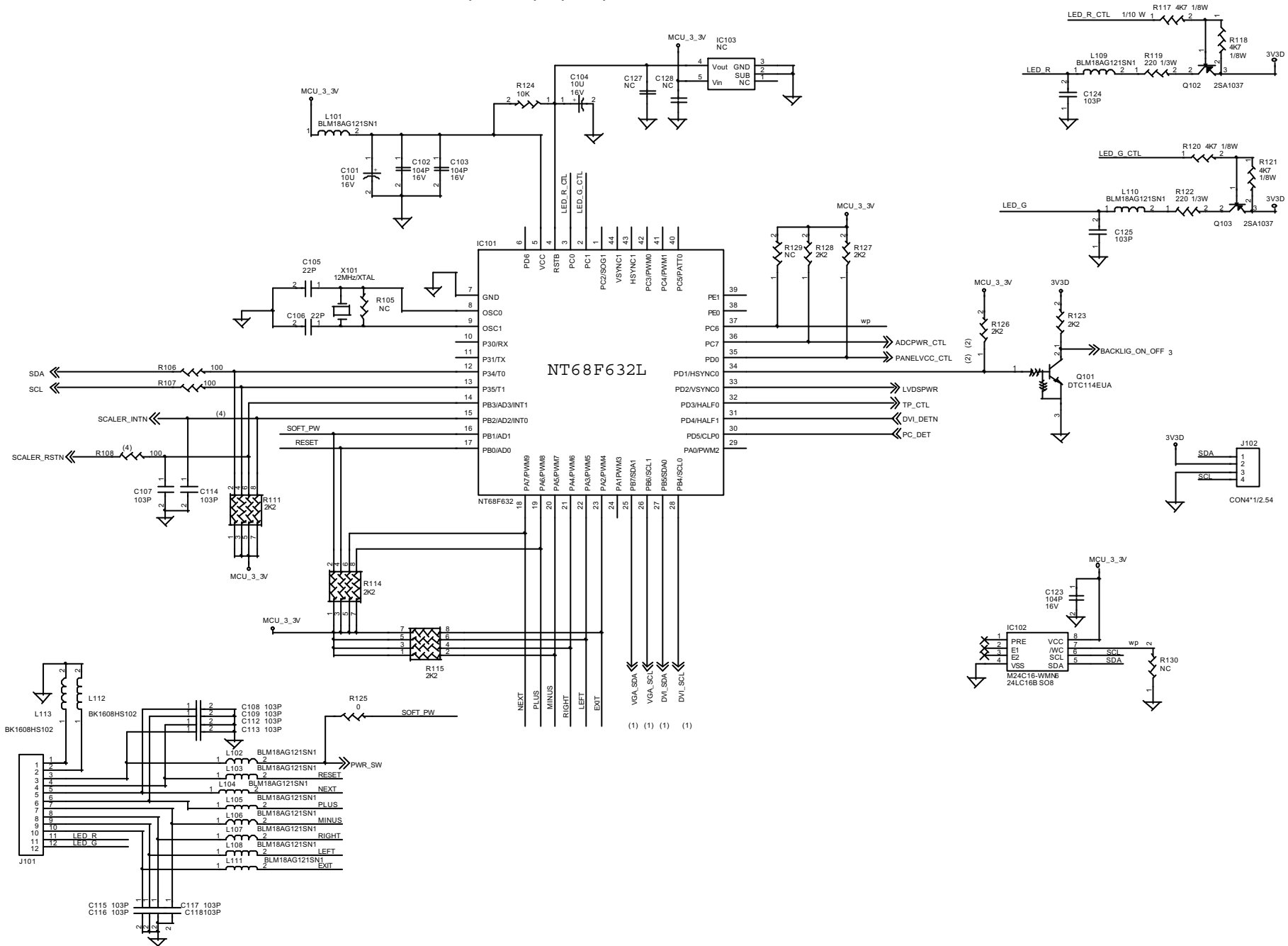
SCHEMATIC DIAGRAM MAIN BOARD (DVI_IN) (1/6)



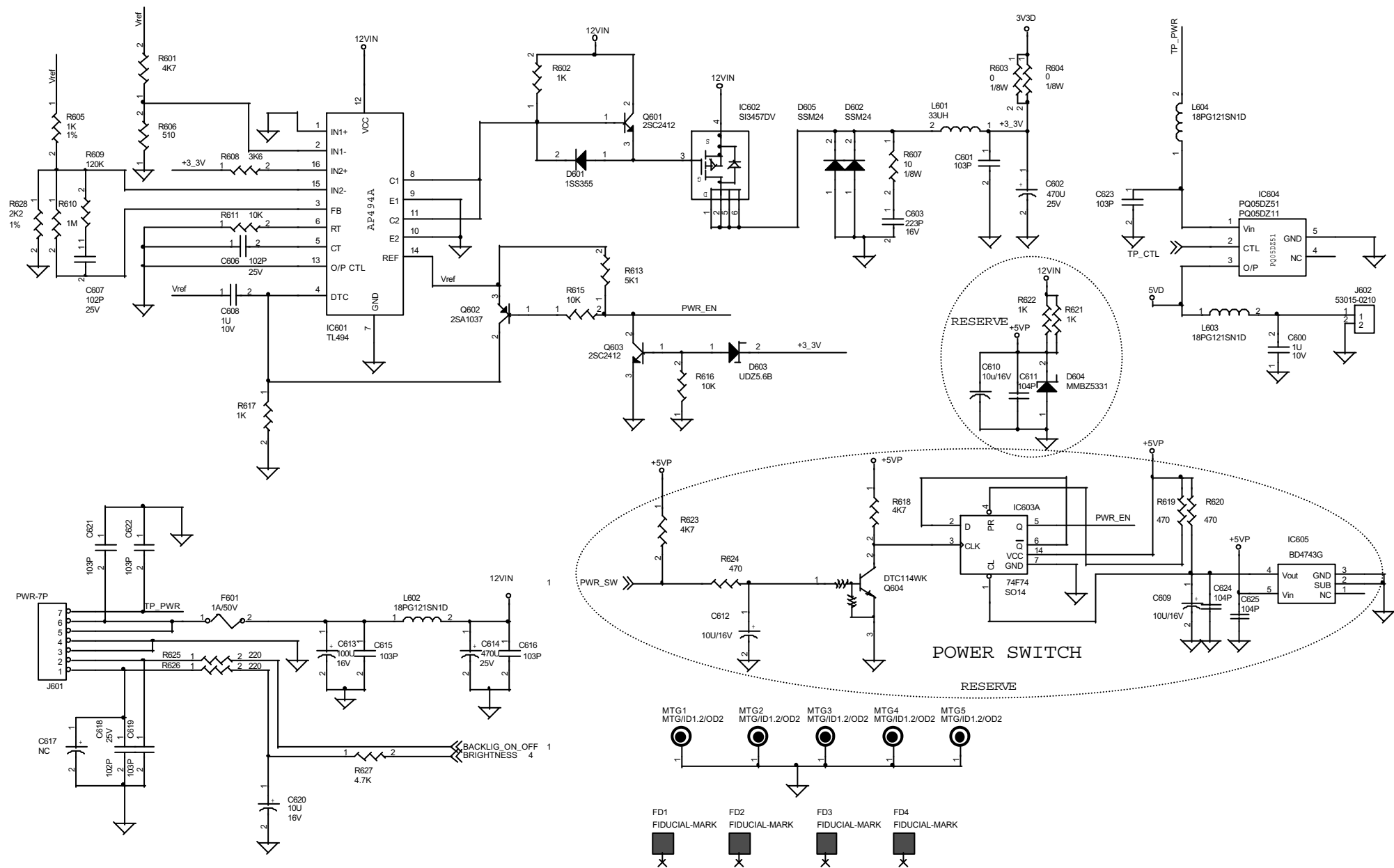
SCHEMATIC DIAGRAM MAIN BOARD (LVDS) (2/6)



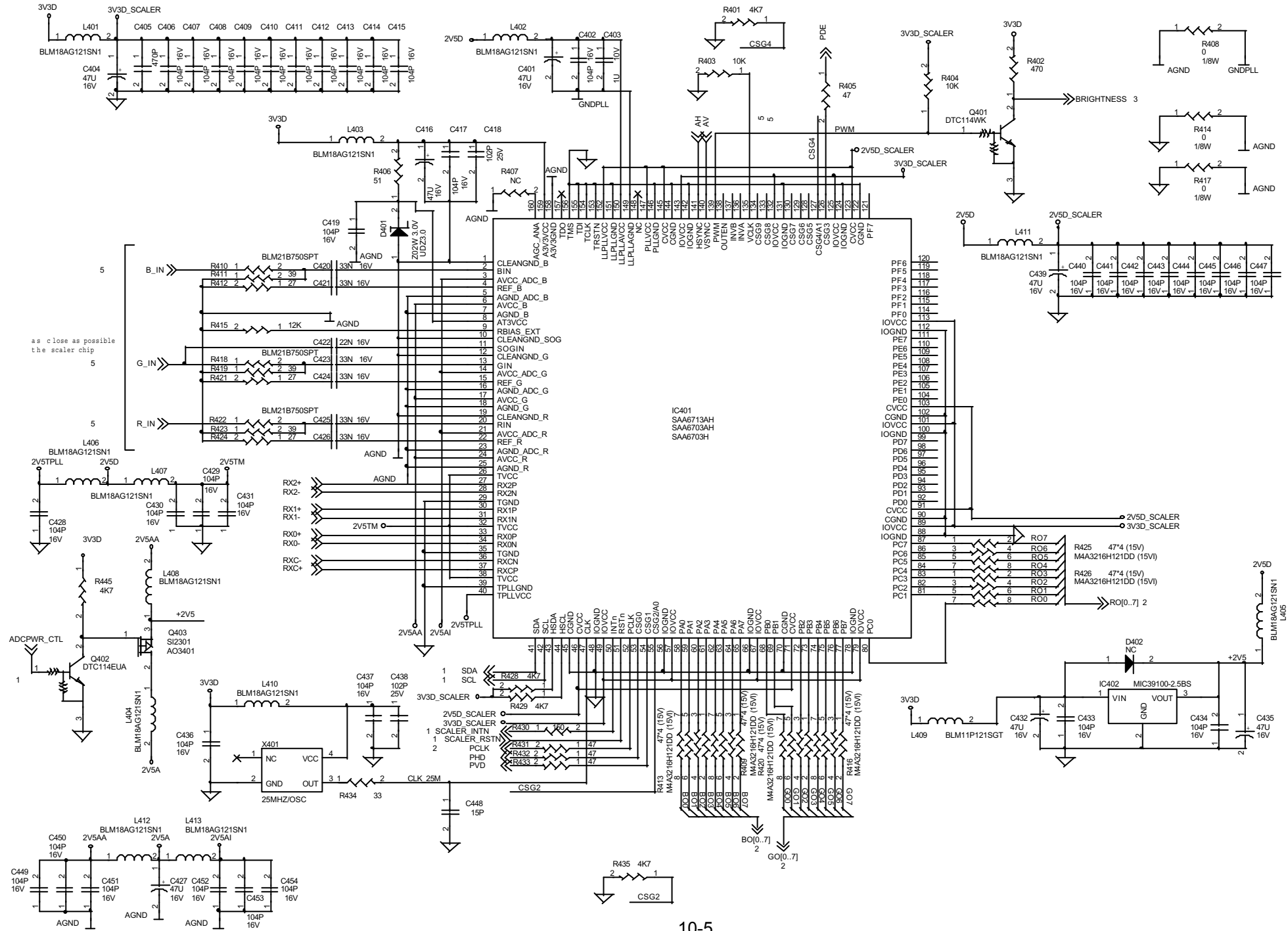
SCHEMATIC DIAGRAM MAIN BOARD (MCU) (3/6)



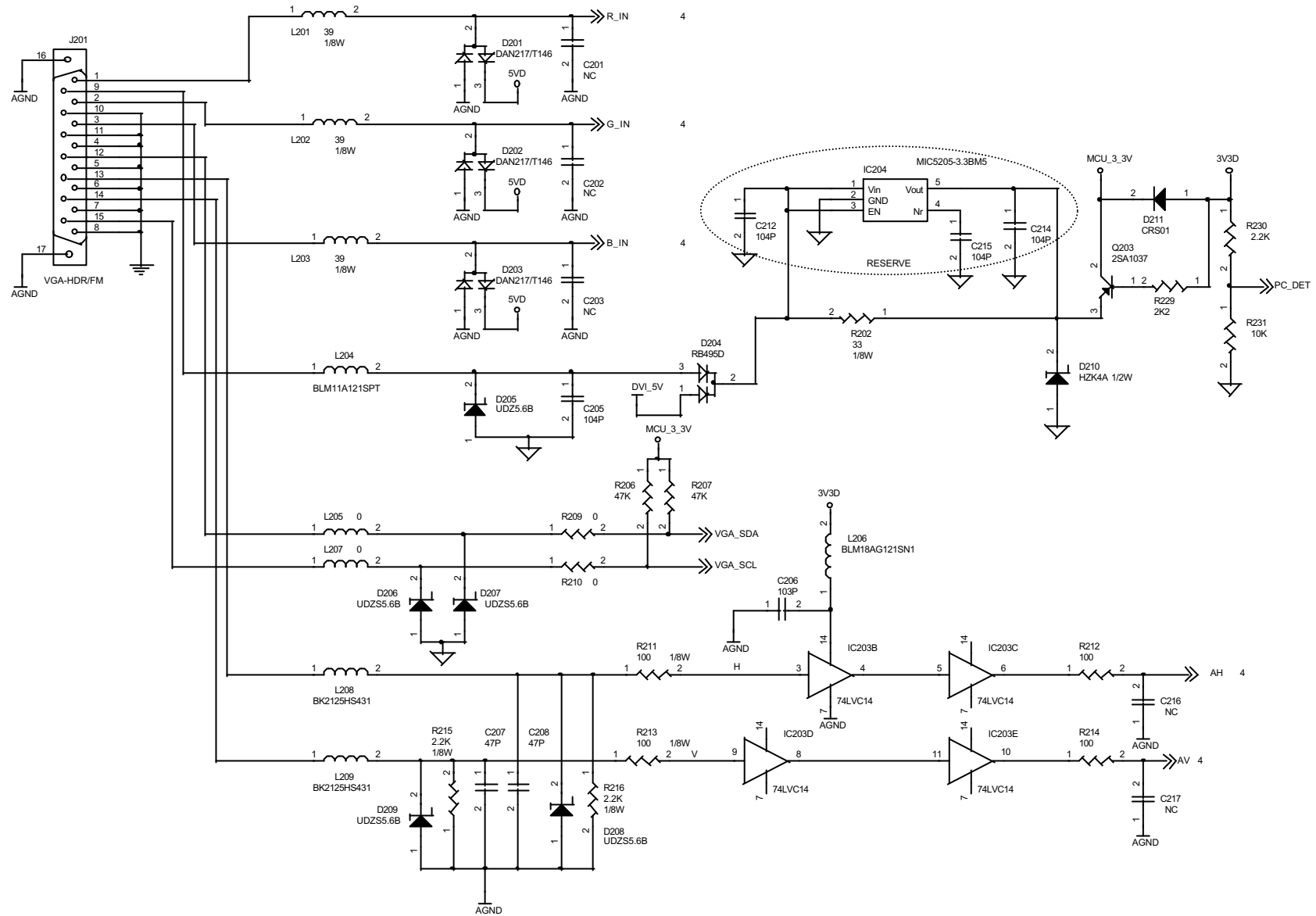
SCHEMATIC DIAGRAM MAIN BOARD (POWER) (4/6)



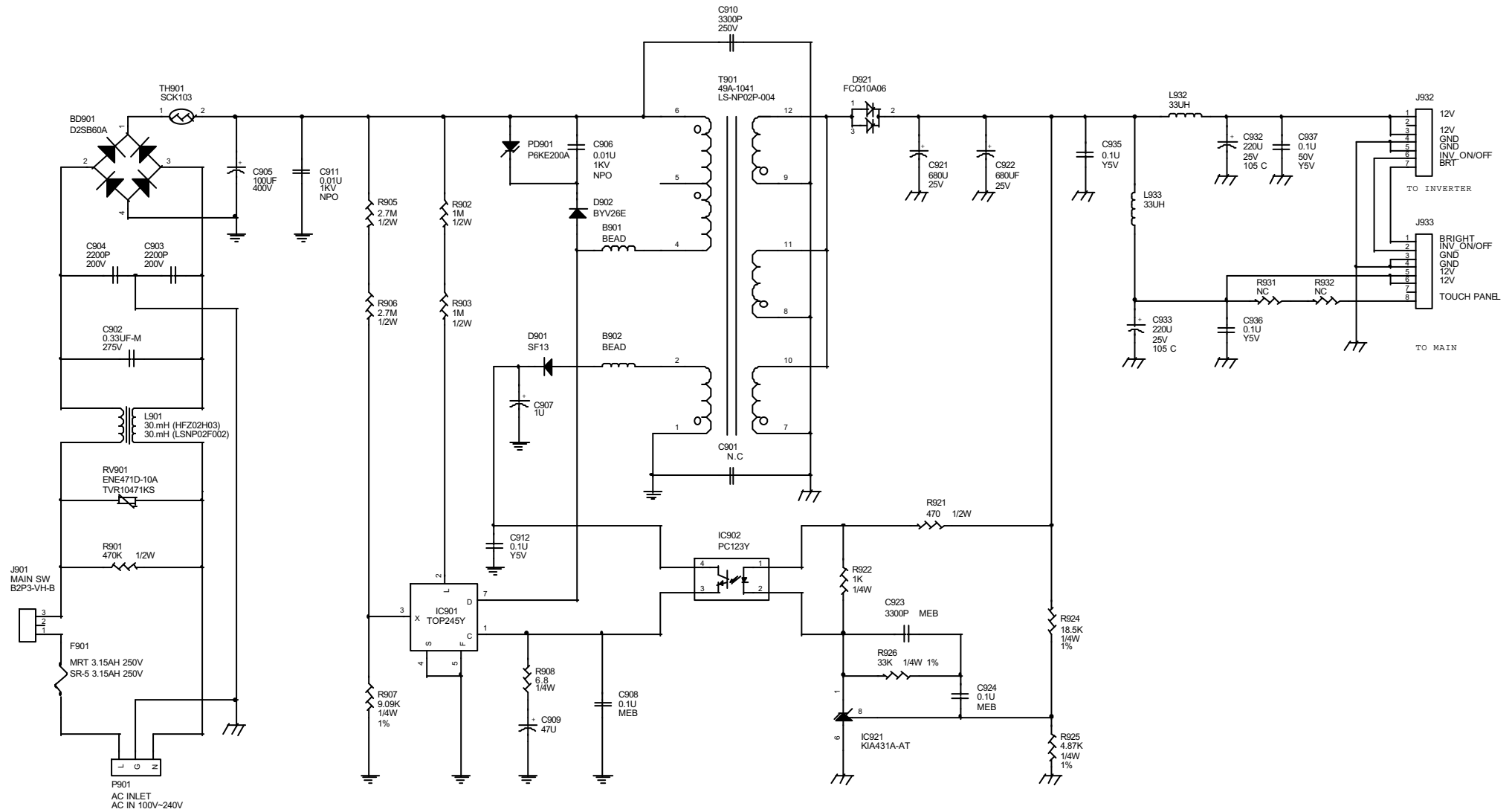
SCHEMATIC DIAGRAM MAIN BOARD (SCALER) (5/6)



SCHEMATIC DIAGRAM MAIN BOARD (VGA) (6/6)



SCHEMATIC DIAGRAM POWER BOARD (1/1)



1

